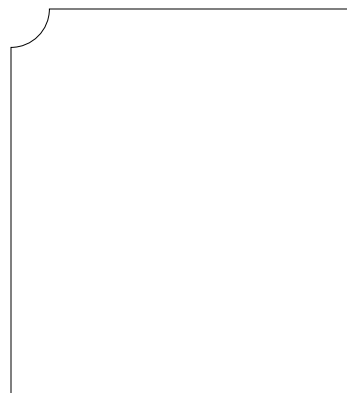
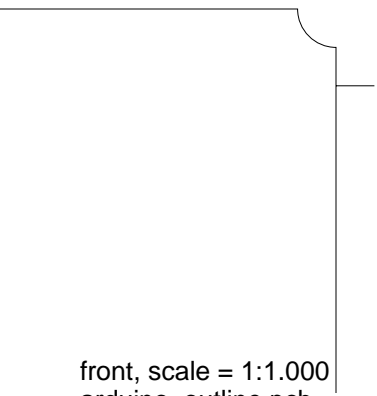
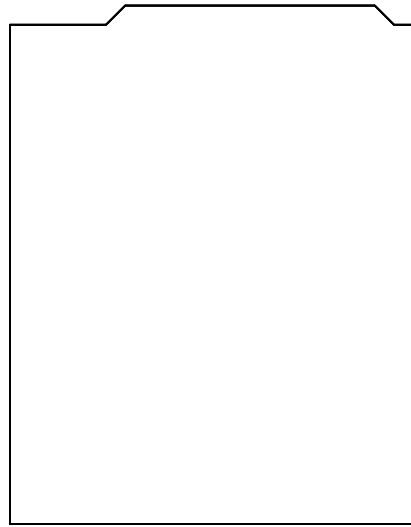
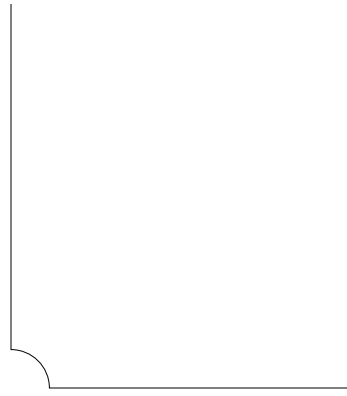
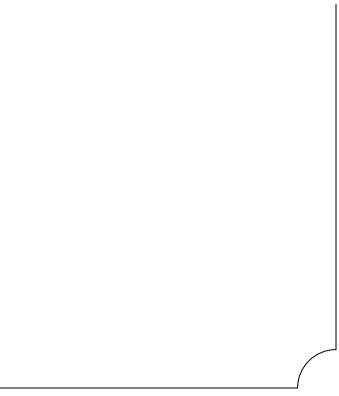
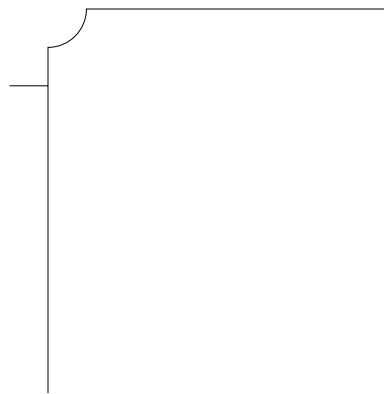
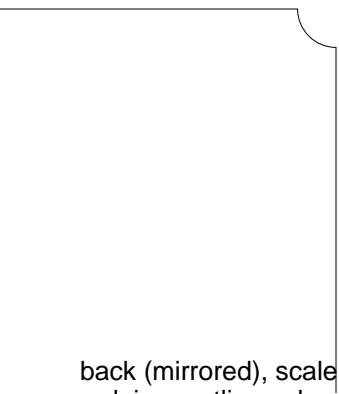
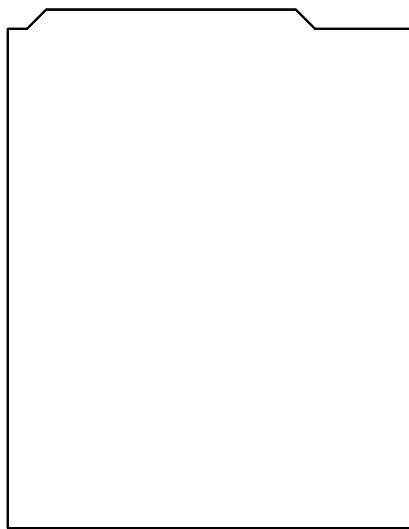
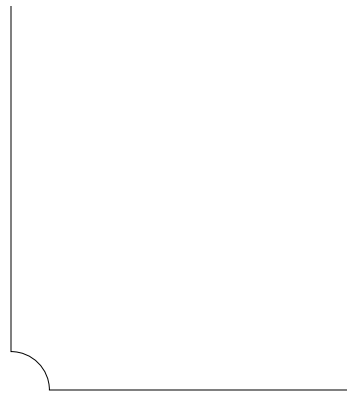
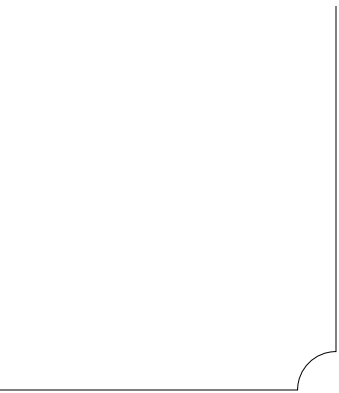
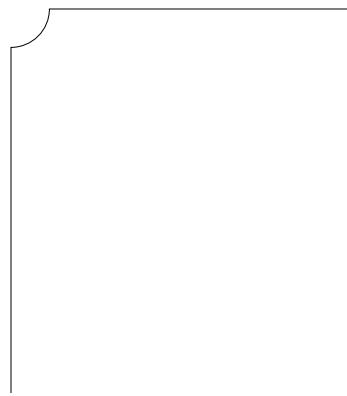
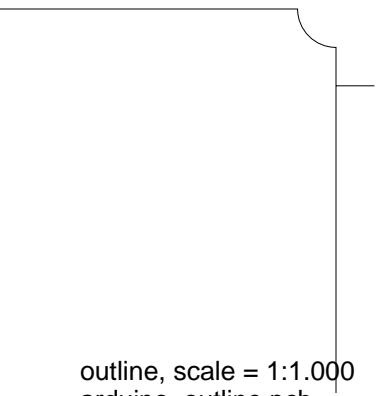
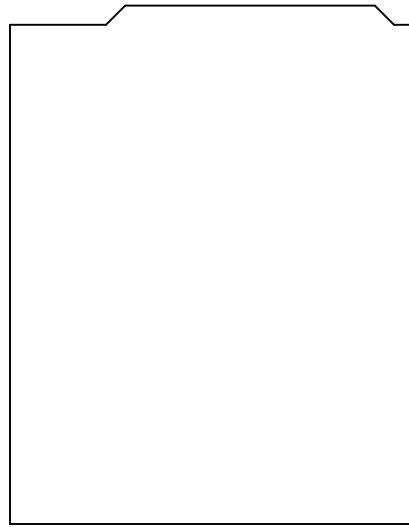
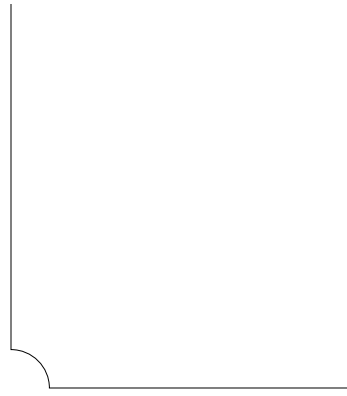
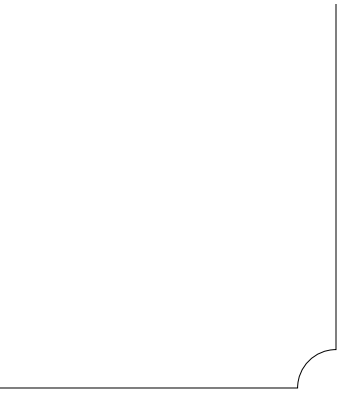


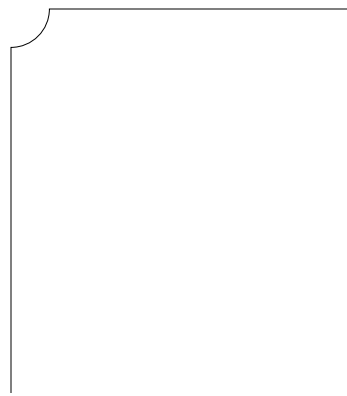
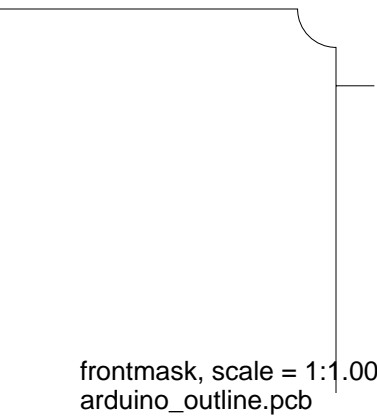
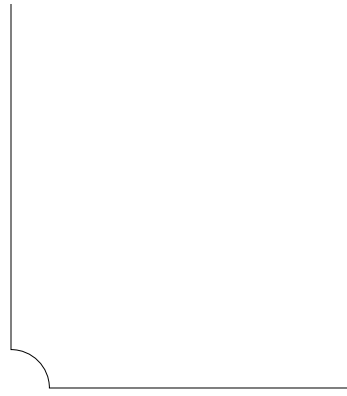
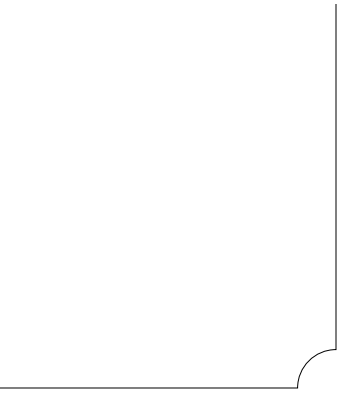
2. component
3. solder
4. outline
5. componentmask
6. soldermask
7. topsilk
8. bottomsilk
9. topassembly
10. bottomassembly
11. fab



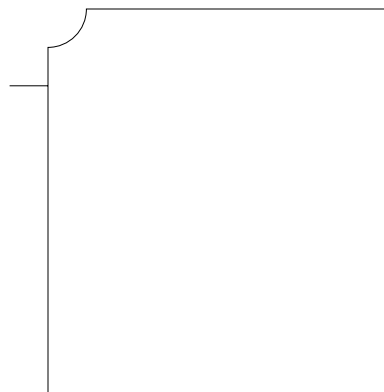
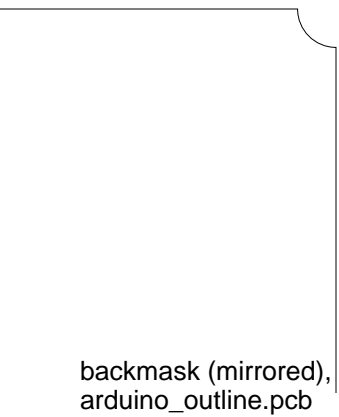
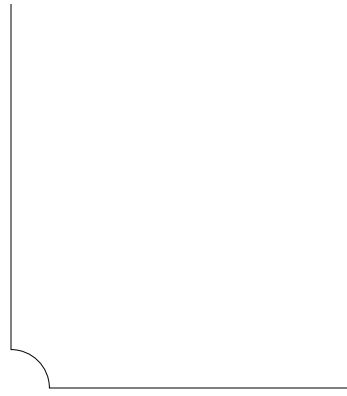
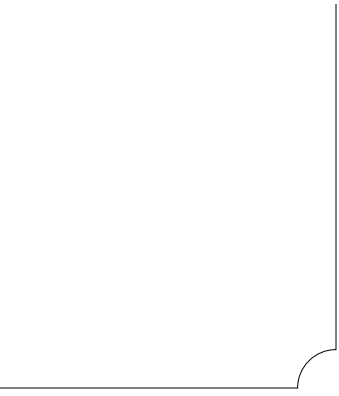
front, scale = 1:1.000
arduino_outline.pcb



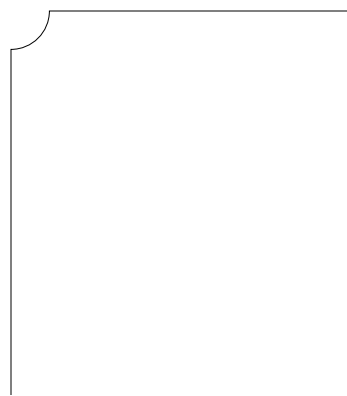
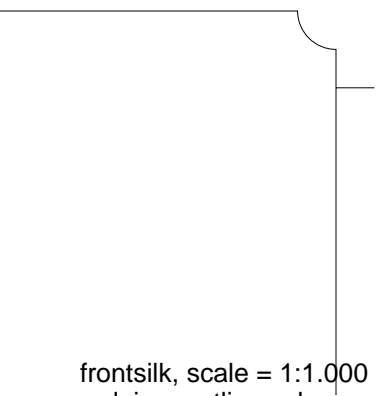
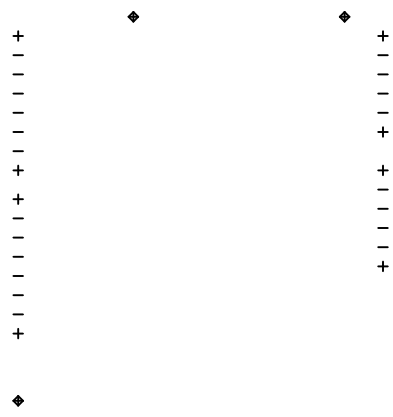
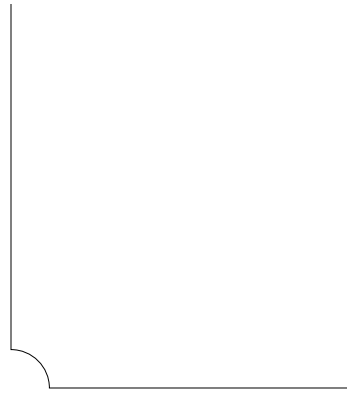
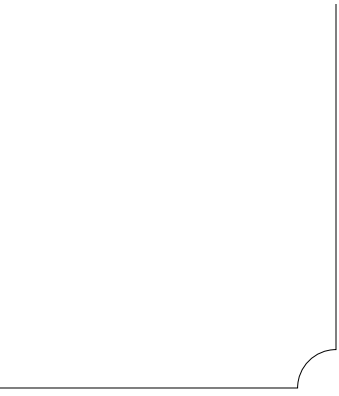


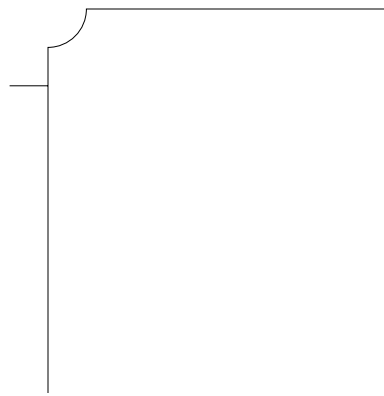
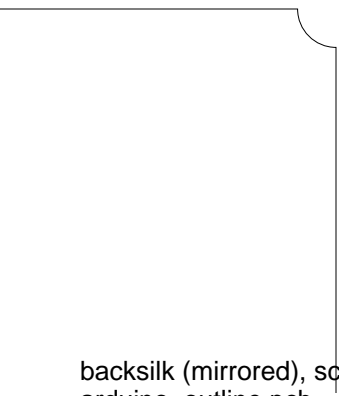
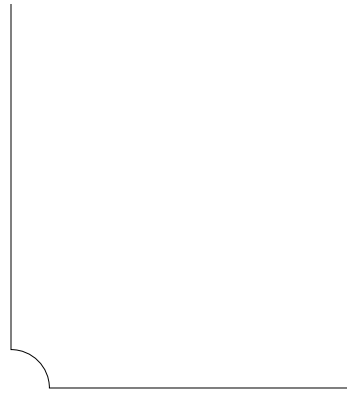
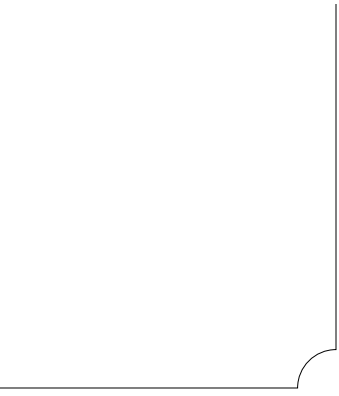


frontmask, scale = 1:1.000
arduino_outline.pcb

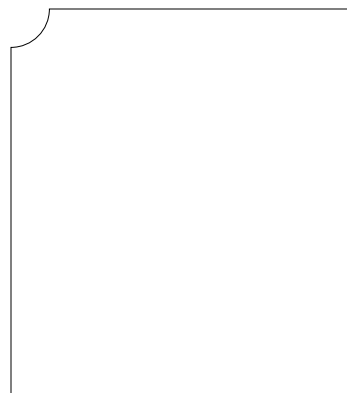
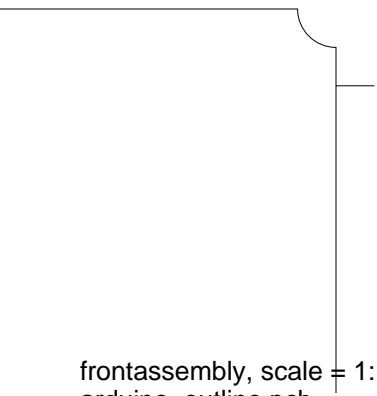
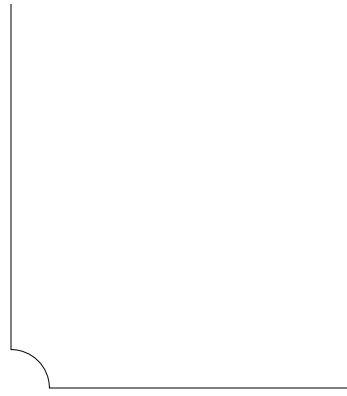
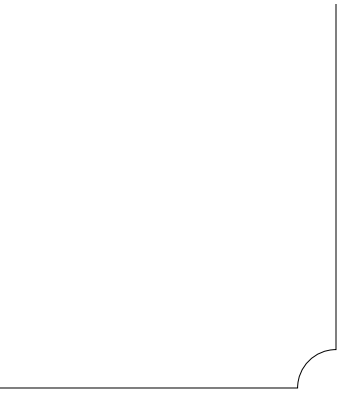


backmask (mirrored), scale = 1:1.000
arduino_outline.pcb

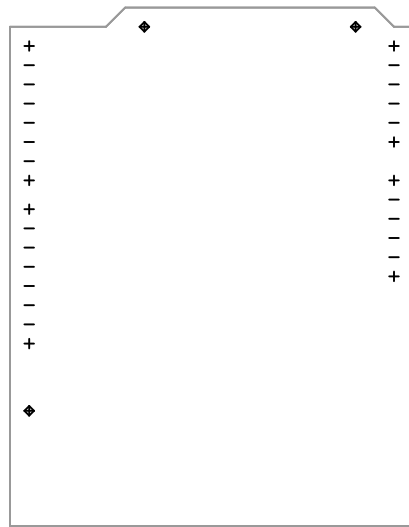


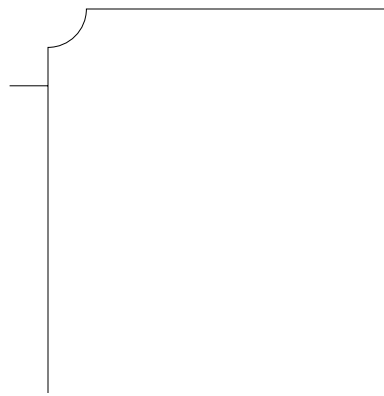
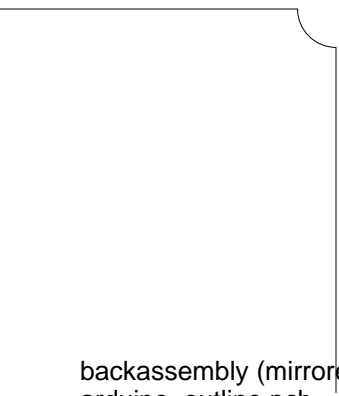
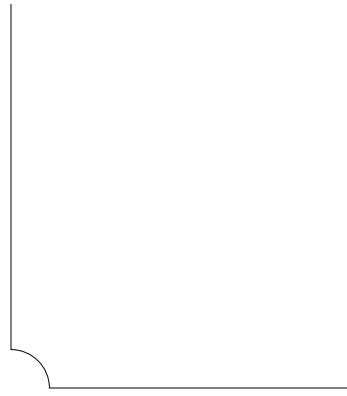
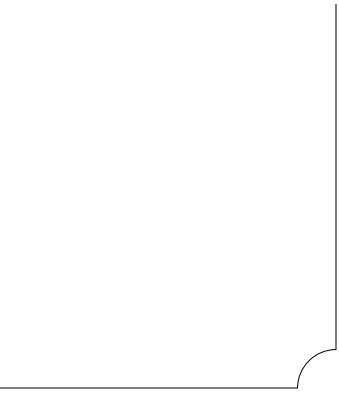


backsilk (mirrored), scale = 1:1.000
arduino_outline.pcb

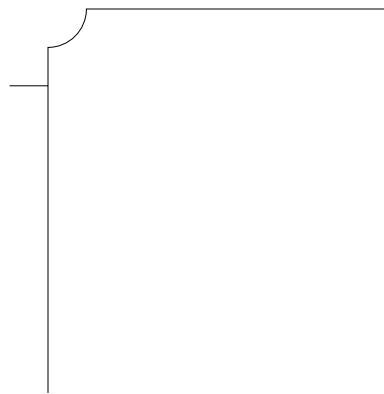
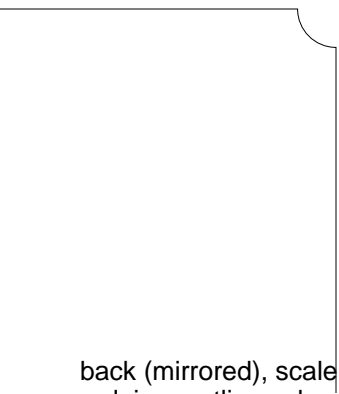
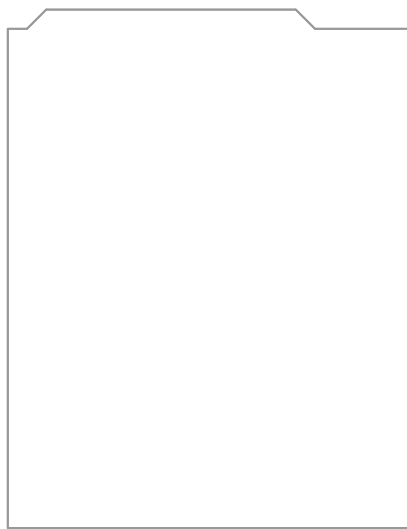
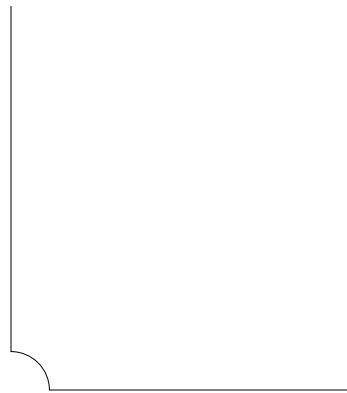
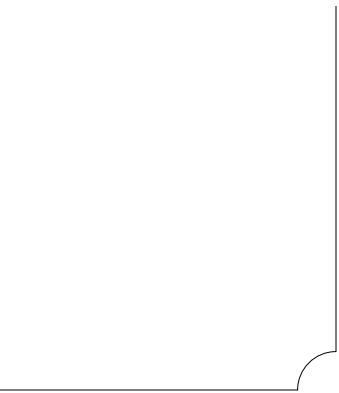


frontassembly, scale = 1:1.000
arduino_outline.pcb





backassembly (mirrored), scale = 1:1.000
arduino_outline.pcb



back (mirrored), scale = 1:1.000
arduino_outline.pcb

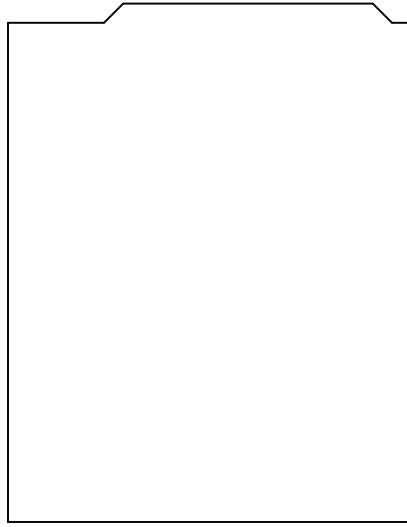
There are 0 different drill sizes used in this layout, 0 holes total

Symbol Diam. (Inch) Count Plated?

Title: (unknown) - Fabrication Drawing

Author: User &

Date: Tue Nov 24 21:25:06 2009 UTC



Board outline is the centerline of this path